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AUG 14 2006

Applicant: MICROBRIDGE TECHNOLOGIES INC.
Serial No.: 10/796,421
International Filing Date: September 10, 2002
35USC§371 date: March 10, 2004
Title: METHOD FOR TRIMMING RESISTORS
Examiner: Paul D. Kim Tel: (571) 272-4565
Agent of Record: Alexandra Daoud Tel: (514) 847-4333

Sent by Fax: (571) 273-8300

MAIL STOP AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450
U.S.A.

RESPONSE

Sir:

In response to the Official Action dated July 24, 2006, in connection with the above-identified patent application, please amend the above-identified application as follows:

Amendment to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 9 of this paper